

# LIQUID CRYSTAL DISPLAY MODULE

# **Standard Product Specification**

PRODUCT NUMBER	LMR2053
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Product Mgr	Quality Mgr	Electrical Eng	Document Control
Date:	Date:	Date:	Date:

## □ Approval for Specification only

□ Approval for Specification and Sample

Sample no.: Date: ISIR no.:

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### **REVISION RECORD**

Rev.	Date	Page	Chapt.	Comment	ECN no.
А	11/08/10			Standard Specification Release, ROHS compliant	E4371

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#### **1 MAIN FEATURES**

UNIT=MM

ITEM	CONTENTS		
Display Format	2 line x 20 characters		
Colour	Monochrome		
Overall Dimensions	80.0 (W) x 21.5 (H) x 5.3 (D)		
Viewing Area	75.0 (W) x 15.0 (H)		
LCD Type	STN		
Mode	Transflective - Positive		
Viewing Angle	6:00		
Duty Ratio	1/16		
Controller / Interface	Sitronix ST7066 / Parallel interface		
Operating Temperature	-20°C~+70°C		
Storage Temperature	-30°C~+80°C		
ROHS Compliant	Yes		

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#### 2 MECHANICAL SPECIFICATION

#### 2.1 MECHANICAL CHARACTERISTICS

ITEM	ITEM CHARACTERISTIC		
Display Format	2 line x 20 characters		
Character Format	mat 5 (W) x 7 (H) with attached cursor		
Overall Dimensions	80.0 (W) x 21.5 (H) x 5.3 (D)	mm	
Viewing Area	75.0 (W) x 15.0 (H)	mm	
Active Area	68.5 (W) x 10.7 (H)	mm	
Character Size	2.95 (W) x 5.15 (H)	mm	
Character Pitch	3.45 (W) x 5.55 (H)	mm	
Dot Size	0.55 (W) x 0.60 (H)	mm	
Dot Pitch	0.60 (W) x 0.65 (H)	mm	
IC Controller/Driver	ST7066	·	

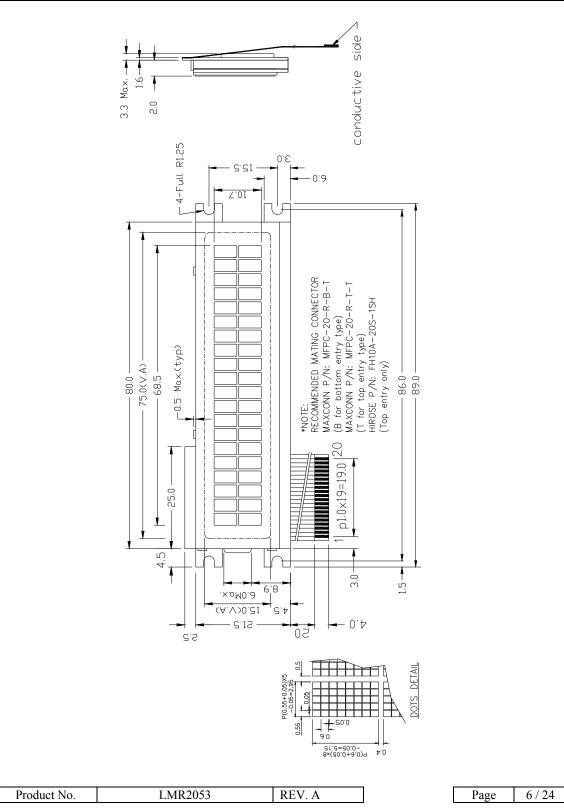
#### 2.2 LABELLING & MARKING

DENSITRON LMR2053 TAIWAN YYMM

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#### 2.3 MECHANICAL DRAWING





#### **3 ELECTRICAL SPECIFICATION**

#### 3.1 ABSOLUTE MAXIMUM RATINGS

				VSS =	0 V, Ta = 25 °C
Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	$V_{DD}$ - $V_{SS}$	0	7.0	V	
LC Driver Supply Voltage	V <sub>DD</sub> -V <sub>O</sub>	0	13.0	V	
Operating Temperature	Тор	-20	+70	°C	Note 1
Storage Temperature	Tst	-30	+80	°C	Note 2
Static Electricity	Be sure that you are grounded when handling displays.				

Note 1: Background colour changes slightly depending on ambient temperature. This phenomenon is reversible. Ta≤70 °C: 75% RH max

Note 2: Ta≤80 °C: 75% RH max

## 3.2 ELECTRICAL CHARACTERISTICS

				VSS = 0 V, Ta = 25 °C					
Item	Symbol	Condition	Min	Тур	Max	Unit			
Power Supply for Logic	$V_{DD}$ - $V_{SS}$	$Ta = 25^{\circ}C$		4.5	5.0	V			
Input Valtaga	$V_{\mathrm{IH}}$	$Ta = 25^{\circ}C$	2.2	-	$V_{DD}$	V			
Input Voltage	$V_{IL}$	$Ta = 25^{\circ}C$			0.6	V			
Output Voltage	$V_{\rm OH}$	Іон=0.205mA	2.4			V			
Output Voltage	V <sub>OL</sub>	IoL=1.2mA			0.4	V			
Current Consumption	* I <sub>DD</sub>	$V_{DD} = 5V$		1		mA			

\* I<sub>DD</sub> measurement condition is for all pattern ON

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#### 3.3 RECOMMENDED LC DRIVE VOLTAGE (VDD-VO)

VDD=5.0±0.25V

Temperature	NTN-H
$T_a = -20^{\circ}C$	4.5
$T_a = 0^{\circ}C$	4.5
$T_a = 25^{\circ}C$	4.5
$T_a = 50^{\circ}C$	4.5
Ta=70°C	4.5

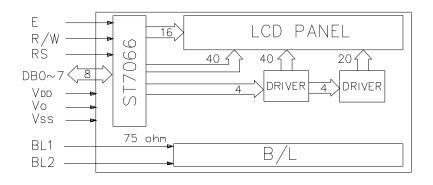
#### 3.4 INTERFACE PIN ASSIGNMENT

Pin No.	Symbol	I/O	Function
1	N/C	-	No Connection
2	N/C	-	No Connection
3	Vss	-	Ground (0V)
4	Vdd	-	Logic Supply Voltage (+5V) / (+3V)
5	Vo	-	LC Drive voltage for contrast adjustment
6	RS	Ι	Register Select 0: Instruction Register
			1: Data Register
7	R/W	Ι	Read / Write 0: Data Write (Module←MPU)
			1: Data Read (Module→MPU)
8	Е	Ι	Enable Signal Active High ( $H \rightarrow L$ )
9	DB0	I/O	Bi-directional data bus line 1
10	DB1	I/O	Bi-directional data bus line 0
11	DB2	I/O	Bi-directional data bus line 3
12	DB3	I/O	Bi-directional data bus line 2
13	DB4	I/O	Bi-directional data bus line 5
14	DB5	I/O	Bi-directional data bus line 4
15	DB6	I/O	Bi-directional data bus line 7
16	DB7	I/O	Bi-directional data bus line 6
17	N/C	-	No Connection
18	N/C	-	No Connection
19	VEE	-	Negative voltage output for models with on-board
			negative voltage generators
20	N/C	-	No Connection

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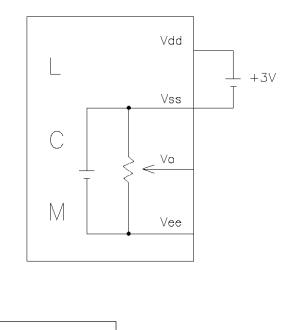


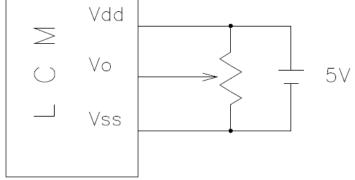
#### 3.5 BLOCK DIAGRAM



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# Recommended Vr : 10K ohm ~ 20K ohm

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## 3.7 TIMING CHARACTERISTICS

#### ST7066U

# AC Characteristics

(TA = 25°C, VCC = 5V)

Symbol	Characteristics	<b>Test Condition</b>	Min.	Тур.	Max.	Unit
	A.	Internal Clock Operation	n			
fosc	OSC Frequency	R = 91KΩ	190	270	350	KHz
		External Clock Operatio	n			
fex	External Frequency	-	125	270	410	KHz
	Duty Cycle		45	50	55	%
$T_{\rm R}, T_{\rm F}$	Rise/Fall Time		18		0.2	μs
	Write Mod	le (Writing data from MPU	to ST706	6U)		
T <sub>G</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
Tpw	Enable Pulse Width	Pin E	140		-	ns
$T_{\rm R}, T_{\rm F}$	Enable Rise/Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS,RW,E	0	-	-	ns
TAH	Address Hold Time	Pins: RS,RW,E	10		-	ns
T <sub>DSW</sub>	Data-Setup Time	Pins: DB0 - DB7	40		-	ns
Тн	Data Hold Time	Pins: DB0 - DB7	10	1	-	ns
	Read Mode	(Reading Data from ST70	066U to N	MPU)		
Τċ	Enable Cycle Time	Pin E	1200	1		ns
TPW	Enable Pulse Width	Pin E	140		-	ns
$T_R,T_F$	Enable Rise/Fall Time	Pin E			25	ns
TAS	Address Setup Time	Pins: RS,RW,E	0	1.75	-	ns
TAH	Address Hold Time	Pins: RS,RW,E	10	-	5	ns
TDDR	Data Setup Time	Pins: DB0 - DB7		43	100	ns
Τ <sub>H</sub>	Data Hold Time	Pins: DB0 - DB7	10	), Th	-	ns
	Interfa	ce Mode with LCD Driver(	ST7065)			
TCWH	Clock Pulse with High	Pins: CL1, CL2	800	-	-	ns
TCWL	Clock Pulse with Low	Pins: CL1, CL2	800		-	ns
T <sub>CST</sub>	Clock Setup Time	Pins: CL1, CL2	500	-		ns
T <sub>SU</sub>	Data Setup Time	Pin: D	300	-	-	ns
TOH	Data Hold Time	Pin: D	300	-	-	ns
TDM	M Delay Time	Pin: M	0		2000	ns

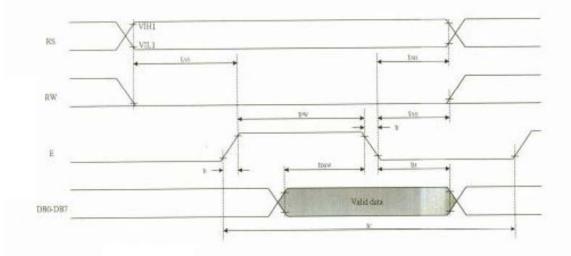
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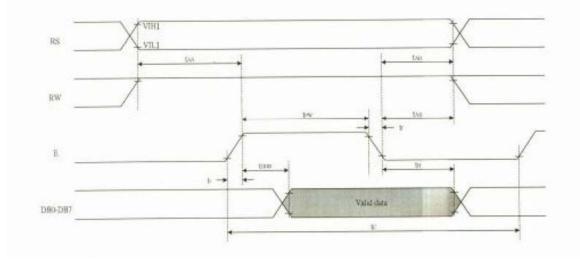
## ST7066U

# Timing Characteristics

#### Writing data from MPU to ST7066U



Reading data from ST7066U to MPU



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## 3.8 CHARACTER FONT

N	0	7	n	66.	-DA
1.4	<u>v</u> .	1	U	00	UA

67-64	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)			0	Ð	P	~	Þ					9	Ξ,	C.	p
0001	(2)				P	Q	æ	-			13	7	ų.	ć,	ä	q
0010	(3)		11	2	B	R	Ь	r			ľ	4	ų	22	P	0
0011	(4)		#		С	5	C	s			.1	ņ	Ť	Ŧ	S	60
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# DENSITRON®

## **4 OPTICAL SPECIFICATION**

#### 4.1 OPTICAL CHARACTERISTICS

		Ta = 2					= 25 °C	
Item		Symbol	Condition	Min	Тур	Max	Unit	Note
		θ1	CR≥2		30		deg	1
Viewing	an al a	θ2	CR≥2		30		deg	1
Viewing Angle	θ3	CR≥2		30		deg	2	
		θ4	CR≥2		30		deg	2
Contrast Ratio	STN(-H)	CR	Ta = 25°C	5			-	3
	Tr	$Ta = 25^{\circ}C$		150	250		4	
Response Time		Tf	$Ta = 25^{\circ}C$		150	250	ms	4
Driving M	lethod	Duty			1/16			
LCD Type	2		STN –	Transfle	ctive - Po	ositive		
Viewing I	Direction			6:0	00			

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12H

(ø\_ 180.

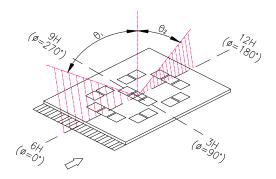
(ø\_=90°)

Note 1: definition of viewing angle  $\theta 1 \& \theta 2$ 

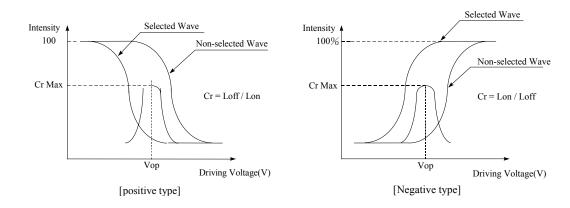
Note 2: definition of viewing angle  $\theta$ 3 &  $\theta$ 4

(ø.

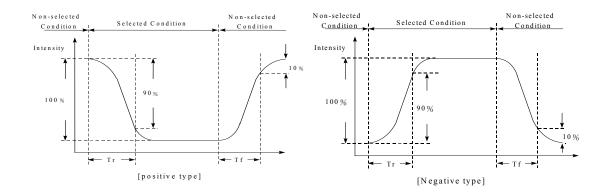
, 6H ≥0°,



Note 3: definition of contrast ratio (CR)



Note 4: definition of response time



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#### 5 QUALITY ASSURANCE SPECIFICATION

#### 5.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

#### 5.2 DELIVERY ASSURANCE

#### 5.2.1 Delivery inspection standards

- MIL-STD-105E, general inspection level II, single sampling level;
- IPC-AA610 rev. C, class 2 electronic assemblies standard

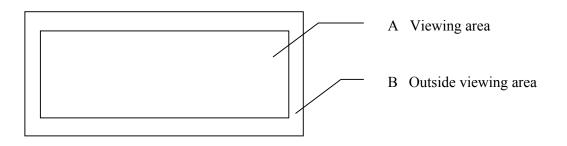
The quality assurance levels are shown below:

Class	AQL (%)
Critical defect	0.5%
Major defect	1.0%
Minor defect	1.5%
TOTAL	2.0%

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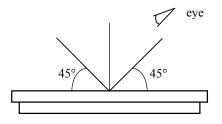


#### 5.2.2 Zone definition



#### 5.2.3 Visual inspection

- Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- Inspect the module at 45° right and left, top and bottom.
- Use the optimum viewing angle during the contrast inspection.



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## 5.2.3.1 Standard of appearance inspection

Units: m	m	1			
Class	Item		Criteria	1	
Minor	Packing &	Outside & inside package	e Presence of pro	oduct no., lot no.,	quantity
Critical	Label	Product must not be mixed		quantity must not	be different from
		that indicated on the labe			
Major	Dimension	Product dimensions must	t be according to sp	becification and di	rawing
Major	Electrical	Product electrical charact	teristics must be ac	cording to specifi	cation
Critical	LCD Display	Missing lines or wrong p	atterns on LCD dis	splay are not allow	ved
Minor	Black spot, white spot,	Round type: as per follow $\emptyset = (X+Y)/2$	ving drawing		
	dust			cceptable quantity	
			Size	Zone A	Zone B
		+	Ø<0.1	Any number	
		Y	0.1<Ø<0.2	2	Any number
		→ <sub>V</sub>   ← †	0.2<Ø<0.25	1	
		А	0.25<Ø	0	
		Line type: as per following	ng drawing		
		***		ple quantity	
		W Length	Width	Zone A	Zone B
			W≤0.02	Any number	-
		$L \leq 3.0$	$0.02 < W \le 0.03$	2	Any number
		$L = \frac{L \leq 2.5}{}$	0.03 <w≤0.05 0.05<w< td=""><td>As round type</td><td>-    </td></w<></w≤0.05 	As round type	-
			0.03 ~ W	As found type	
		Total accep			
Minor	Polariser	Scratch on protective film	n is permitted		
	scratch	Scratch on polariser: sam	e as No. 1		
Minor	Polariser	$\emptyset = (X+Y)/2$			
	bubble			cceptable quantity	
			Size	Zone A	Zone B
		↓ <b>↓</b>	Ø<0.2	Any number	4
		Y	0.2<Ø<0.5	2	Any number
			0.5<Ø<1.0	1	
			1.0<Ø	0 quantity: 3	
			-		

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Class	Item		Criter	ia	
Minor	Segment deformation	1.a. Pin hole on segmented	display		
		W: segment width			
		$\emptyset = (A+B)/2$	1	Acceptable quantity	7
		A B	Width	Ø	
			W≤0.4	$\emptyset \leq 0.2$ and	
			W>0.4	Ø≤0.25 and	
		= V / A	-	le quantity: 1 defect $\emptyset$ under 0.10 mm a	
Minor	Segment	1b. Pin hole on dot matrix d	lisplay		
	deformation	₩ <0.0	5 <u> </u>	Acceptable	quantity
			- <u>i</u>	Size	
			þ.	a,b<0.1	Any number
				(a+b)/2≤0.1 0.5<∅<1.0	Any number
				Total acceptable	5
		3. Alignment layer defect $\emptyset = (a+b)/2$		Accept $a \ge b$ $a < b$ AcceptableSize $\emptyset \le 0.4$ $0.4 < \emptyset \le 1.0$ $1.0 < \emptyset \le 1.5$ $1.5 < \emptyset \le 2.0$	a/b≤4/3 a/b>4/3
		$\begin{array}{c c} \hline \\ \hline $			
Minor	Colour uniformity	Level of sample for approva	al set as limit s	ample	
Critical	Backlight	The backlight colour should	l correspond to	the product specifi	cation
Critical		Flashing and or unlit backli	ght is not allow	ved	
Minor	]	Dust larger than 0.25 mm is	not allowed		
Major	COB	Exposed wire bond pad is n	ot allowed		
Major	1	Insufficient covering with r		wed (wire bond line	e exposed)
Minor		Dust or bubble on the resin			· /
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Class	Item	Criteria						
Major	PCB	No unmelted solde	r paste should be pre	esent on PCB				
Critical		Cold solder joints,	missing solder conne	ections, or oxidation	n are not allowed			
Minor	And	No residue or solde	er balls on PCB are a	llowed				
Critical		Short circuits on components are not allowed						
Minor	Tray			Size	Quantity			
	particles		On trav	Ø<0.2	Any number			
			On tray	Ø>0.25	4			
			On diaplay	Ø≥0.25	2			
			On display	L = 3	1			

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## 5.3 DEALING WITH CUSTOMER COMPLAINTS

#### 5.3.1 Non-conforming analysis

Purchaser should supply Densitron with detailed data of non-conforming sample. After accepting it, Densitron should complete the analysis in two weeks from receiving the sample.

If the analysis cannot be completed on time, Densitron must inform the purchaser.

#### 5.3.2 Handling of non-conforming displays

If any non-conforming displays are found during customer acceptance inspection which Densitron is clearly responsible for, return them to Densitron.

Both Densitron and customer should analyse the reason and discuss the handling of nonconforming displays when the reason is not clear.

Equally, both sides should discuss and come to agreement for issues pertaining to modification of Densitron quality assurance standard.

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## 6 RELIABILITY SPECIFICATION

#### 6.1 RELIABILITY TESTS

Test Item	Test Condition	Evaluation and assessment
High Temperature Operation	70°C <b>±</b> 2°C, 240 HR	No abnormalities in function* and appearance
Low Temperature Operation	-20°C±2°C, 240 HR	No abnormalities in function* and appearance
Thermal Shock Storage	-30°C (30 min)->25°C (5 min) ->80°C (30 min)->25°C (5 min) 5 cycles	No abnormalities in function* and appearance
Vibration	10 Hz~55Hz 0.3mm / 1 Octave 55 Hz~500 Hz 3g / 1 Octave 20 cycles per axis	No abnormalities in function* and appearance

#### 6.2 LIFE TIME

Item	Description
1	Function, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions of room temperature (25±10 °C), normal humidity (45±20% RH), and in area not exposed to direct sunlight.

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## 7 PART NUMBER DESCRIPTIONS FOR AVAILABLE OPTIONS

# LMR2053022C203456

$\bigcirc$	<b>Polarizer Type</b> B = Transflective: light background no backlight
2	Not applicable- LEAVE BLANK
3	<b>Fluid Type and Power Supply</b> H = STN-H with +5VDC or +3VDC operation
4	Fluid Type N = STN-H
5	<b>Background Color for STN</b> G = Gray background
6	Supply Voltage

/5 = 5VDC Supply Voltage
/3=3VDC Supply Voltage

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#### 8 HANDLING PRECAUTIONS

#### Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

#### Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface. When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean. Design the system so that no input signal is given unless the power supply voltage is applied.

#### Caution during LCD cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane. Do not wipe the display surface with dry or hard materials that will damage the polariser surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

#### Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminal to VDD or VSS. Do not input any signals before power is turned on.

Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

#### Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

#### Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life. Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation. Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged. If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once. Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

#### Storage

Store the display in a dark place where the temperature is  $25^{\circ}C \pm 10^{\circ}C$  and the humidity below 50%RH.Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

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