

# LIQUID CRYSTAL DISPLAY MODULE

# **Product Specification**

CUSTOMER			
PRODUCT NUMBER	LMR4145	LMR4145	
CUSTOMER APPROVAL		Date	

INTERNAL APPROVALS			
Product Mgr	Doc Control	Electr. Eng	
Bruno Anthony Recaldini Perkins		Alan Wang	
Date: 11/03/2013	Date: 11/03/2013	Date: 11/03/2013	



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#### **REVISION RECORD**

Rev.	Date	Page	Chapt.	Comment	ECR no.
A	11/03/2013			First issue	

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# 1 MAIN FEATURES

ITEM	CONTENTS	
Display Format	4-line 20-character display	
Overall Dimensions	77.0 x 47.0 x 18.2 max. (Edge LED version)	
Viewing Area	58.6(W) x 21.1(H)	
LCD type	Transflective	
Mode	Grey / Yellow-green	
Viewing Direction	6 O'clock	
Duty ratio	1/16	
Controller / Driver IC	Driver IC Sitronix ST7066 / ST7063	
Backlight type	Edge LED	
Backlight colour	Yellow-green	
Operating temperature	Standard: 0 °C~ 50 °C / Wide: -20 °C~ 70 °C	
Storage temperature	Standard: -20 °C~ 70 °C / Wide: -30 °C ~ 80 °C	

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# **2 MECHANICAL SPECIFICATION**

# 2.1 MECHANICAL CHARACTERISTICS

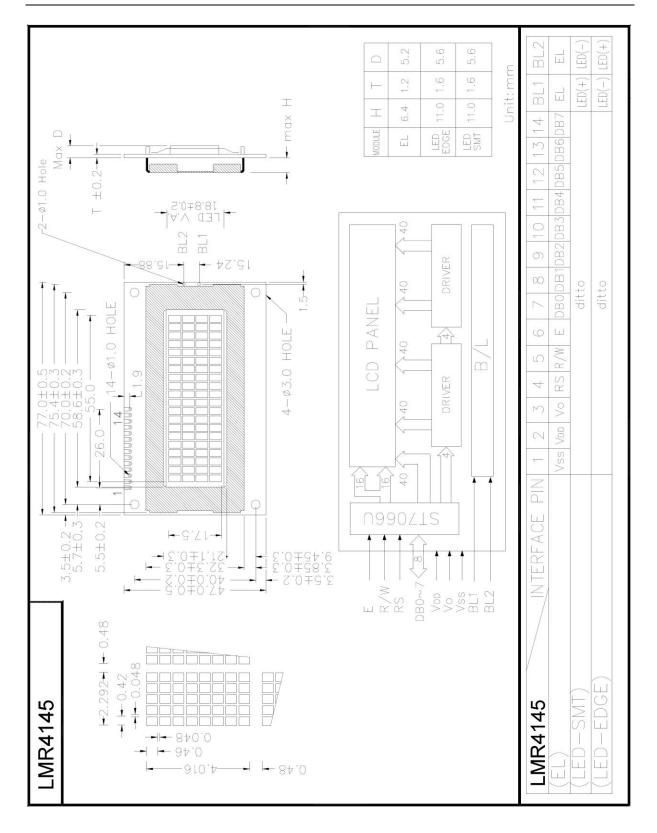
ITEM	CHARACTERISTIC	UNIT
Display Format	4 line x 20 characters	-
Character Format	5 (W) x 8 (H) with attached cursor	dots
Overall Dimensions	77.0 x 47.0 x 18.2 max. (Edge LED version)	mm
Viewing Area	58.6 x 21.1	mm
Active Area	55.0 x 17.5	mm
Character Size	2.292 x 4.016	mm
Character Pitch	2.772 x 4.496	mm
Dot Size	0.42 x 0.46	mm
Dot Pitch	0.468 x 0.508	mm
Weight	-	g
Controller / Driver IC	Sitronix ST7066 / ST7063	

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## 2.2 MECHANICAL DRAWING



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# **3 ELECTRICAL SPECIFICATION**

# 3.1 ABSOLUTE MAXIMUM RATINGS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Min	Max	Unit	Note	
Power Supply Voltage	$V_{DD}$ - $V_{SS}$	0	7.0	V		
Operating Temperature	Тор	0	50	°C	Standard temperature range	
Storage Temperature	Tst	-20	70	°C		
Operating Temperature	Тор	-20	70	°C	***	
Storage Temperature	Tst	-30	80	°C	Wide temperature range	
Static Electricity	Be sure that you are grounded when handling displays.					

## 3.2 ELECTRICAL CHARACTERISTICS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Condition	Min	Тур	Max	Unit
Power Supply for Logic	$V_{DD}$ - $V_{SS}$	Ta = 25 °C	4.75	5.0	5.25	V
Y	$V_{IL}$	Ta = 25 °C	0	-	0.6	V
Input Voltage	$V_{IH}$	Ta = 25 °C	$0.7V_{DD}$	-	$V_{DD}$	V
LCD Module Driving Voltage	V <sub>DD</sub> -V <sub>O</sub>	Ta = 25 °C	0	-	10.0	V
Current Consumption	* I <sub>DD</sub>	$V_{DD} = V$ $V_{LCD} = V$	-	3	-	mA

<sup>\*</sup>  $I_{DD}$  measurement condition is for all pattern ON

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# 3.3 RECOMMENDED LC DRIVE VOLTAGE ( $V_{DD}$ - $V_O$ )

 $V_{DD} = 5.0 \pm 0.25 V$ 

Temperature	Standard Temp. Range	Wide Temp. Range
Ta = -20 °C	-	5.8
Ta = 0 °C	4.5	5.2
Ta = 25 °C	4.2	4.8
Ta = 50 °C	3.9	4.5
Ta = 70 °C	-	4.2

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# 3.4 INTERFACE PIN ASSIGNMENT

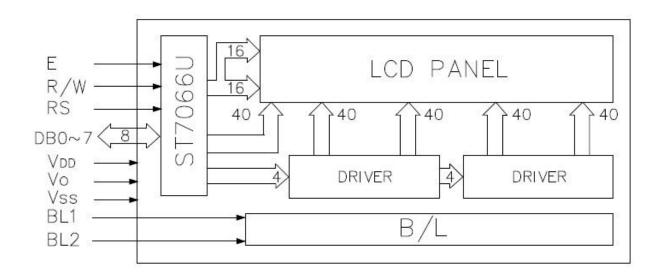
No.	Symbol	I/O	Function
1	$V_{SS}$	-	Ground (0 V)
2	$V_{DD}$	-	Logic Supply Voltage (+5 V)
3	$V_{O}$	-	Voltage Level for LCD Control Adjustment
4	RS	I	Register Select 0: Instruction Register 1: Data Register
5	R/W	I	Read / Write 0: Data Write (Module-MPU)  1: Data Read (Module-MPU)
6	Е	I	Enable Signal Active High (H-L)
7 ~ 14	DB0 ~ 7	I/O	Bi-directional data bus line 0~7
BL1	LED(-)	-	Cathode of LED B/L
BL2	LED(+)	-	Anode of LED B/L

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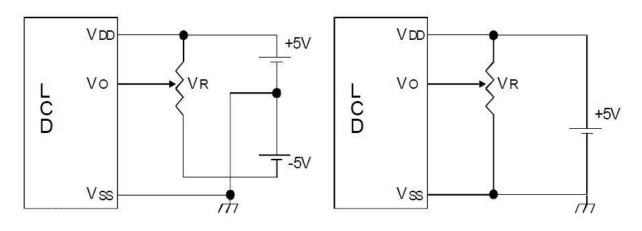
## 3.5 BLOCK DIAGRAM



## 3.6 POWER SUPPLY CIRCUIT

Wide Temperature Range

#### Standard Temperature Range



Recommended  $V_R$ : 10K~20K ohm

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# 3.7 CHARACTER GENERATOR ROM MAP

NO.7	066-	0A														
67-64 63-60	טטטט	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)							••••								
0100	(5)															
0101	(6)															
0110	7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	7)															
1111	(8)															

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# 3.8 TIMING CHARACTERISTICS

 $(Ta = 25 \, ^{\circ}C, VDD = 5V)$ 

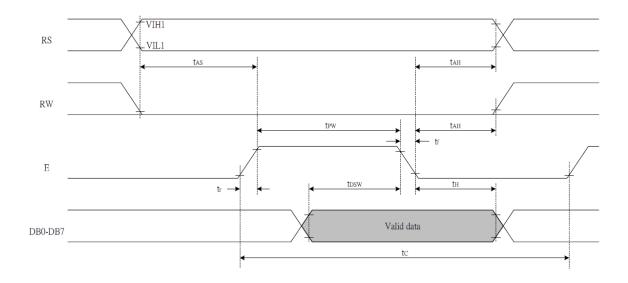
Symbol	Characteristics	<b>Test Condition</b>	Min.	Тур.	Max.	Unit
	Intern	al Clock Operation	-	•		•
$f_{OSC}$	OSC Frequency	R=91kΩ	190	270	350	kHz
	Extern	al Clock Operation				
$f_{EX}$	External Frequency	-	125	270	410	kHz
	Duty Cycle	-	45	50	55	%
$T_R, T_F$	Rise/Fall Time	-	-	-	0.2	μs
	Write Mode (Writin	ng data from MPU to	ST7066	5U)		
T <sub>C</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
$T_{PW}$	Enable Pulse Width	Pin E	140	-	-	ns
T <sub>R</sub> , T <sub>F</sub>	Enable Rise/Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS, RW, E	0	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins: RS, RW, E	10	-	-	ns
$T_{DSW}$	Data Setup Time	Pins: DB0-DB7	40	-	-	ns
T <sub>H</sub>	Data Hold Time	Pins: DB0-DB7	10	-	-	ns
	Read Mode (Readi	ng data from ST7066	U to MI	PU)		
$T_{\rm C}$	Enable Cycle Time	Pin E	1200	-	-	ns
$T_{PW}$	Enable Pulse Width	Pin E	140	-	-	ns
$T_R, T_F$	Enable Rise/Fall Time	Pin E	-	-	25	ns
$T_{AS}$	Address Setup Time	Pins: RS, RW, E	0	-	-	ns
$T_{AH}$	Address Hold Time	Pins: RS, RW, E	10	-	-	ns
$T_{DDR}$	Data Setup Time	Pins: DB0-DB7	-	-	100	ns
$T_{H}$	Data Hold Time	Pins: DB0-DB7	10	-	-	ns
	Interface Mode	e with LCD Driver (S	T7065)			
$T_{CWH}$	Clock Pulse with High	Pins: CL1, CL2	800	-	-	ns
T <sub>CWL</sub>	Clock Pulse with Low	Pins: CL1, CL2	800	-	-	ns
T <sub>CST</sub>	Clock Setup Time	Pins: CL1, CL2	500	-	-	ns
T <sub>SU</sub>	Data Setup Time	Pin D	300	-	-	ns
$T_{DH}$	Data Hold Time	Pin D	300	-	-	ns
$T_{DM}$	M Delay Time	Pin M	0	-	2000	ns

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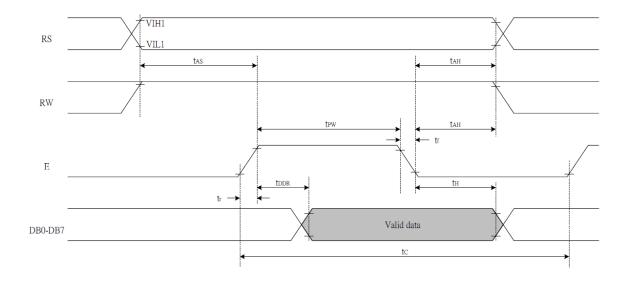
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#### • Writing data from MPU to ST7066U



#### Reading data from ST7066U to MPU



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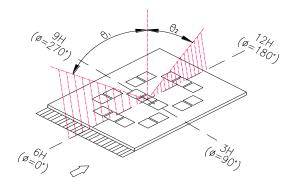
# **4 OPTICAL SPECIFICATION**

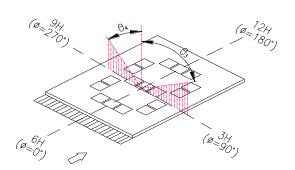
# 4.1 OPTICAL CHARACTERISTICS

Ta = 25 °C

Item	Symbol	Condition	Min	Тур	Max	Unit	Note
	θ1	CR≥2	-	40	-	deg	1
Viewing Angle	θ2	CR≥2	-	40	-	deg	1
Viewing Angle	θ3	CR≥2	-	30	-	deg	2
	θ4	CR≥2	-	30	-	deg	2
Contrast Ratio	CR	Ta = 25 °C	-	7	-	-	3
Daspansa Tima	Tr	Ta = 25 °C	-	140	220		4
Response Time	Tf	Ta = 25 °C	-	210	340	ms	4
Driving Mathod	Duty			1/16			
Driving Method	Bias	_		1/5			

Note 1: definition of viewing angle  $\theta 1 \& \theta 2$  Note 2: definition of viewing angle  $\theta 3 \& \theta 4$ 



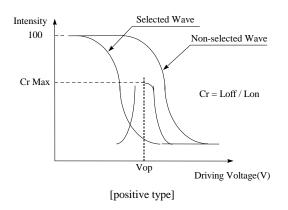


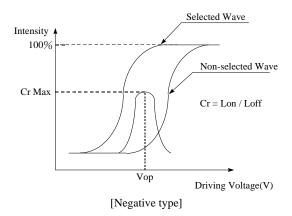
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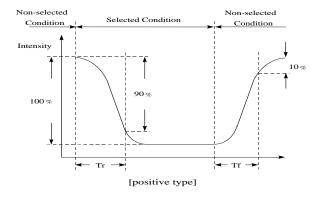


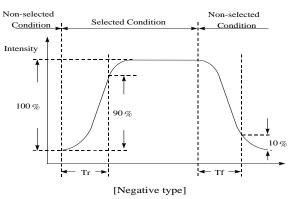
## Note 3: definition of contrast ratio (CR)





## Note 4: definition of response time





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# **5 BACKLIGHT SPECIFICATION**

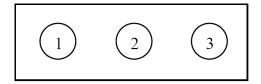
## **5.1 BACKLIGHT CHARACTERISTICS**

The backlight comprises of Edge LED

Item	Symbol	Condition	Min	Тур	Max	Unit	Note
LED Input Voltage	$V_{LED}$	Ta = 25 °C	-	5.0	-	V	1
LED Input Current	$I_{LED}$	Ta = 25 °C	-	200	-	mA	-
Luminous Intensity	$I_{V}$	Test when connecting after 3 min Ta = 25 °C (max. contrast)	-	150	-	cd/m²	1
Brightness Uniformity	-	Ta = 25 °C, I = 40.0 mA	80	-	-	%	2
Life time	-	Ta = 25 °C, I = 40.0 mA Humidity: 30%RH ~ 85%RH	-	50,000	-	hr	3
Colour	Yellow-green						

#### Note:

- 1. Average luminous intensity of 3 points when B/L is used at the beginning.
- 2. Brightness uniformity =  $(MIN / MAX) \times 100\%$ .
- 3. Half of the original average brightness.



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# **6 LABELLING & MARKING**

DENSITRON Product number Country YYMM

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## 7 QUALITY ASSURANCE SPECIFICATION

#### 7.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

#### 7.2 DELIVERY ASSURANCE

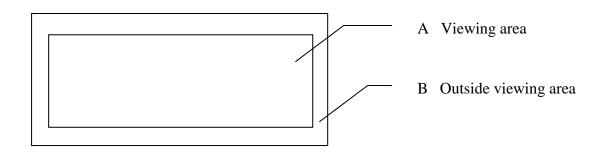
#### 7.2.1 Delivery inspection standards

- MIL-STD-105E, general inspection level II, single sampling level;
- IPC-AA610 rev. C, class 2 electronic assemblies standard

The quality assurance levels are shown below:

Class	AQL (%)
Critical defect	0.65%
Major defect	1.0%
Minor defect	2.5%
TOTAL	2.5%

#### 7.2.2 Zone definition



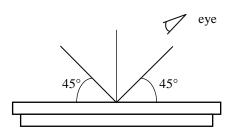
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# 7.2.3 Visual inspection

- Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- Inspect the module at 45° right and left, top and bottom.
- Use the optimum viewing angle during the contrast inspection.



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# 7.2.3.1 Standard of appearance inspection

Units: mm

Class	Item	Criteria					
Minor	Packing &	Outside & inside package   Presence of product no., lot no., quantity					
Critical	Label	Product must not be mixed with others and quantity must not be different from					
		that indicated on the label					
Major	Dimension	Product dimensions must be according to specification and drawing					
Major	Electrical	Product electrical characteristics must be according to specification					
Critical	LCD Display	Missing lines or wrong patterns on LCD display are not allowed					
Minor	Black spot, white spot,	Round type: $\emptyset = (X+Y)/2$	as per follow 2	ving drawing			
	dust			A	cceptable quantity	1	
				Size	Zone A	Zone B	
			<u> </u>	Ø<0.1	Any number		
			Y	0.1<Ø<0.2	2	1	
			<del> </del>	0.2<Ø<0.25	1	Any number	
		X		0.25<Ø	0		
		Line type: as per following drawing  Acceptable quantity					
		, W	Length	Width	Zone A	Zone B	
		7//~		W≤0.02	Any number		
		$ / \searrow $	L≤3.0	0.02 <w≤0.03< td=""><td></td><td>A 1</td></w≤0.03<>		A 1	
			L≤2.5	0.03 <w≤0.05< td=""><td>2</td><td>Any number</td></w≤0.05<>	2	Any number	
		L		0.05 <w< td=""><td>As round type</td><td></td></w<>	As round type		
		Total acceptable quantity: 3					
Minor	Polariser			n is permitted			
7.6	scratch	•	olariser: sam	e as No. 1			
Minor	Polariser	$\emptyset = (X+Y)/2$	2				
	bubble				cceptable quantity		
				Size	Zone A	Zone B	
			<u>+</u>	Ø<0.2	Any number		
			Y	0.2<Ø<0.5	2	Any number	
		→ <sub>X</sub>	Т	0.5<Ø<1.0	1		
		<b>A</b>		1.0<Ø	0		
		Total acceptable quantity: 3					

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Class	Item		Criter	ia	
Minor	Segment deformation	1.a. Pin hole on segmented display			
		W: segment width			
		$\emptyset = (A+B)/2$		Acceptable quantity	
			Width	Ø 50.2	
			W≤0.4 W>0.4	$\emptyset \le 0.2$ and $\emptyset < 0.25$ and	
				$\varnothing \leq 0.25$ and le quantity: 1 defection	
		A MARINE TO THE PROPERTY OF TH	_	$\varnothing$ under 0.10 mm a	
Minor	Segment	1b. Pin hole on dot matrix	display		
	deformation	₩ <0.	05	Acceptable	quantity
				Size a,b<0.1	Any number
				$(a+b)/2 \le 0.1$	Any number
				0.5<Ø<1.0	3
				Total acceptable	quantity: 7
		2. Segments / dots with di	fferent width		
				Accep	table
				a≥b	a/b≤4/3
				a <b< td=""><td>a/b&gt;4/3</td></b<>	a/b>4/3
		3. Alignment layer defect			
		$\emptyset = (a+b)/2$		Acceptable	quantity
			1	Size	Any number
				Ø≤0.4 0.4<Ø≤1.0	5
				1.0<Ø≤1.5	3
				1.5<∅≤2.0	2
				Total acceptable	quantity: 7
Minor	Colour uniformity	Level of sample for appro	val set as limit s	ample	
Critical	Backlight	The backlight colour shou	ld correspond to	the product specifi	cation
Critical	1	Flashing and or unlit backlight is not allowed			
Minor	1	Dust larger than 0.25 mm	_		
Major	COB	Exposed wire bond pad is			
Major	1	Insufficient covering with resin is not allowed (wire bond line exposed)			
Minor	-	Dust or bubble on the resi			<u> </u>
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Class	Item	Criteria				
Major	PCB ~	No unmelted solde	No unmelted solder paste should be present on PCB			
Critical		Cold solder joints,	Cold solder joints, missing solder connections, or oxidation are not allowed			
Minor		No residue or solde	No residue or solder balls on PCB are allowed			
Critical	<b>XX</b>	Short circuits on components are not allowed				
Minor	Tray			Size	Quantity	
	particles		Ø<0.2		Any number	
			On tray	Ø>0.25	4	
			On diaplay	Ø≥0.25	2	
			On display	L = 3	1	
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#### 7.3 DEALING WITH CUSTOMER COMPLAINTS

#### 7.3.1 Non-conforming analysis

Purchaser should supply Densitron with detailed data of non-conforming sample. After accepting it, Densitron should complete the analysis in two weeks from receiving the sample.

If the analysis cannot be completed on time, Densitron must inform the purchaser.

#### 7.3.2 Handling of non-conforming displays

If any non-conforming displays are found during customer acceptance inspection which Densitron is clearly responsible for, return them to Densitron.

Both Densitron and customer should analyse the reason and discuss the handling of non-conforming displays when the reason is not clear.

Equally, both sides should discuss and come to agreement for issues pertaining to modification of Densitron quality assurance standard.

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## **8 RELIABILITY SPECIFICATION**

#### **8.1 RELIABILITY TESTS**

Standard Temperature Range:

Test Item	Test Condition	Evaluation and assessment
High Temperature Operation	240 HR, 50 °C +/- 2 °C	No abnormalities in function* and appearance
Low Temperature Operation	240 HR, 0 °C +/- 2 °C	No abnormalities in function* and appearance
Thermal Shock Storage	0 °C (30 min.) -> 25 °C (5 min.) -> 50 °C (30 min.) -> 25 °C (5 min.) 5 cycle	No abnormalities in function* and appearance
Vibration	10 Hz ~ 55 Hz 0.3mm / 1 Octave 55 Hz ~ 500 Hz 3g / 1 Octave 20 cycles / per axis	No abnormalities in function* and appearance

Wide Temperature Range:

Test Item	<b>Test Condition</b>	<b>Evaluation and assessment</b>
High Temperature Operation	240 HR, 70 °C +/- 2 °C	No abnormalities in function* and appearance
Low Temperature Operation	240 HR, -20 °C +/- 2 °C	No abnormalities in function* and appearance
Thermal Shock Storage	- 20 °C (30 min.) -> 25 °C (5 min.) -> 70 °C (30 min.) -> 25 °C (5 min.) 5 cycle	No abnormalities in function* and appearance
Vibration	10 Hz ~ 55 Hz 0.3mm / 1 Octave 55 Hz ~ 500 Hz 3g / 1 Octave 20 cycles / per axis	No abnormalities in function* and appearance

<sup>\*</sup> Current consumption < 2 times initial value

## 8.1.1 MTBF of liquid crystal panel

50,000 hours, 90% Confidence Level at 25 °C, 65% RH Max.

If any of the following occurs after the MTBF test, the LCD is deemed to be failed.

- Current consumption increase three times than initial value
- Damaged glass plug and/or polarizer on the LCD glass
- Non-operational display

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<sup>\*</sup> Contrast > ½ initial value



# 8.2 LIFE TIME

Item	Description
1	Function, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions of room temperature (25±10 °C), normal humidity (45±20% RH), and in area not exposed to direct sunlight.
2	Function, performance, appearance, etc. shall be free from remarkable deterioration within 5,000 hours under ordinary operating and storage conditions of 70 °C temperature, normal humidity (45±20% RH), and in area not exposed to direct sunlight.

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# 9 PART NUMBER DESCRIPTIONS FOR AVAILABLE OPTIONS

# LMR4145①24C20345

- ① POLARIZER TYPE
  B = Transflective
- **BACKLIGHT COLOUR**G = Yellow-green
- FLUID TYPE AND TEMPERATURE RANGE
  S = STN with +5V DC operation
  H = STN-H with ±5V DC operation
- FLUID TYPE AND TEMPERATURE COMPENSATION
  N = STN
- S BACKGROUND COLOUR FOR STN

G = Grey Background Y = Yellow Background

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#### 10 HANDLING PRECAUTIONS

#### Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

#### Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.

When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean.

Design the system so that no input signal is given unless the power supply voltage is applied.

#### Caution during LCD cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane.

Do not wipe the display surface with dry or hard materials that will damage the polariser surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

#### Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminal to VDD or VSS. Do not input any signals before power is turned on.

Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

#### Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

#### Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life.

Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation.

Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged.

If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once.

Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

#### Storage

Store the display in a dark place where the temperature is  $25^{\circ}C \pm 10^{\circ}C$  and the humidity below 50%RH.

Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

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